1. DMA stands for Direct Memory Access and it is a used for bulk data movement such as disk I/O it allows processes to access the main memory, independent of the CPU. Multiprogramming is a form of parallel processing in which several programs run at the same time on the same processor. Multiprogramming would not be practical on a computer that does not support DMA because if every process must wait for the CPU to access the main memory then it slows down all the processes and not all the processes are running in parallel or at all anymore.
2. It is not essential that a wrapper of a system call is name the same as the underlying system call, e.g. printf() calls the write() system call. After the read() function in libc is called by the user:
   1. The code for read is put into the register
   2. Trap to the kernel
   3. Dispatch and System call handler is invoked
   4. Then the process returns to the caller
3. No, it is not possible for a process to go from a blocking state to a running state. This is because a blocking state means that the process is waiting for some event to occur, after the event occurs it must give the ready signal to the CPU to tell it that the process is ready to be executed again. Thus, a process can only go to a ready state from a blocked state before running again.

No, it is not possible for a process to go from a ready state to a blocking state. This is because if a process is already in a ready state then it is ready to be executed. Thus, a process must always go from a ready state to a running state before it can go to a blocking state.

1. Context switching allows the illusion of sharing a single CPU or limited number of CPUs among many processes. If the number of CPUs is less than the number of process then context switching is needed to implement multitasking. The operating system maintains a context or state for each process, usually in some form of PCB. When OS switches between process A and B, OS saves the A’s state in A’s PCB and restores B’s state from B’s PCB.
2. aa